

EAST - [default.wsp:1]

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Active

L1: (64611) (semiconductor or integrated adj circuit) and comparat\$4

L6: (13859) 1 and driver

L11: (7959) 6 and memory

L16: (1348) 11 and (addor or subtractor)

L21: (227) 16 and integrator

L26: (210) 21 and switch\$4

L31: (135) 26 and analog adj signal\$1

L36: (123) 31 and digital adj signal\$1

L41: (114) 36 and delay

L46: (109) 41 and filter\$4

L51: (4) 46 and 324/\$.ccls.

L56: (105) 46 not 51

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Default Highlight all hit terms initially

46 not 51

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Inventor

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11

US 65357

200303

169

Hybrid/fiber coa

370/210

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Dapper, Mark

US 65102

200301

Communication sy

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Geile, Michael

US 64938

200212

Method and appar

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Kassatly, Lor

US 64874

200211

Communication sy

455/424

235/380

Dapper, Mark

US 64842

200211

Signal transmiss

713/600

709/400

Tamura, Hiroto

US 64773

200211

Communication sy

455/7

370/342

Roberts, Haro

US 64670

200210

Method for adjus

725/131

Geile, Michael

US 64566

200209

Adaptive vector

370/335

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Lomp, Gary R.

US 64345

200208

Fast fourier tra

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Dapper, Mark

US 64185

200207

Hybrid fiber/coa

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Roberts, Haro

US 64100

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Hybrid fiber/coa

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Roberts, Haro

File

Details

HTML

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NUM

10/054,131